Call for Papers

Workshop on Communication Architecture for Clusters (CAC 2009)

To be held in conjunction with the 23rd IEEE Int'l Parallel and Distributed Processing Symposium (IPDPS 2009)

Rome, Italy, May 25, 2009

THEME Many of the world's largest and fastest computer systems are workstation or server clusters. Numerous research groups in academia, industry, and government are currently engaged in cluster research, seeking new ways to advance the state of the art of cluster communication. The goal of the CAC workshop is to bring together researchers and practitioners working in the areas of communication hardware and software to discuss their latest findings as well as future trends in the design of scalable, high-performance, and cost-effective communication architectures for clusters.

TOPICS OF INTEREST Topics of interest for the workshop include but are not limited to the following: ① low-level communication protocols (e.g., VAPI, Tports, GM, and IP) and higher-level communication layers (e.g., MPI, sockets, put/get, and distributed shared memory), ② communication and architectural issues related to router/switch organization, flow control, congestion control, routing and deadlock handling, load balancing, reliability, QoS support, topology discovery, dynamic reconfiguration, and clustered storage and fileservers, ③ novel network-interface and switch architectures for supporting efficient point-to-point and collective communication, ④ performance measurements, analysis, and early-user experience reports of pre-release and recently released network interfaces, network protocols/emerging

WORKSHOP CO-CHAIRS

Scott Pakin	(LANL)
Craig Stunkel	(IBM)
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PROGRAM COMMITTEE

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Keith Underwood	(Intel)

standards, and routers/switches. Results of both theoretical and practical significance will be considered. Note, however, that papers on topics that are too far removed from cluster communication (e.g., mobile networks, intrusion detection, peer-to-peer networks) will be rejected without review.

PROCEEDINGS The proceedings of this workshop will be published together with the proceedings of other IPDPS 2009 workshops by the IEEE Computer Society Press.

PAPER SUBMISSIONS Submitted manuscripts may not exceed eight single-spaced pages using a 12-point font on 8½ × 11-inch pages, everything included (figures, tables, references, etc.).

IMPORTANT DATES

Paper submission: 8 December 2008 Author notification: 26 January 2009 Camera-ready due: 15 May 2009 Manuscripts must be submitted electronically and in either PostScript or PDF format. Submissions will be judged on correctness, originality, technical strength, significance, quality of presentation, and interest and relevance to the workshop attendees. Submitted papers may not have appeared in or be

under consideration for another workshop, conference, or journal. Manuscript submissions for CAC are being handled by EDAS. To submit a paper, go to https://edas.info/newPaper.php?c=6859&submit=0& and follow the instructions.

ADDITIONAL INFORMATION For more information on CAC 2009 please visit the workshop Web site at http://www.ccs3.lanl.gov/cac2009/ or send e-mail to cac2009@gap.upv.es.